

Fig. 1

LEST AVAILABLE COPY

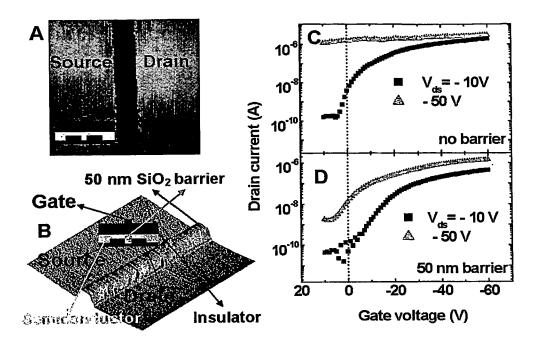


Fig. 2

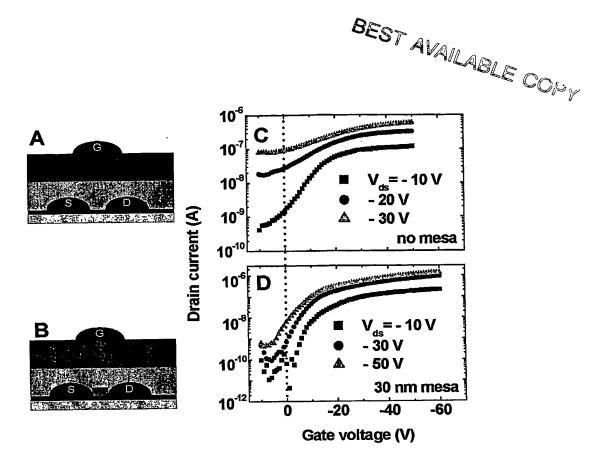


Fig. 3

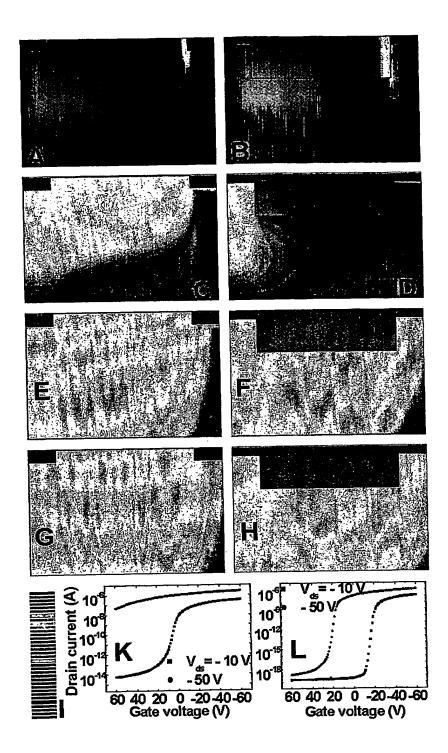
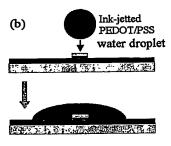
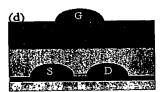


Fig. 4









SiO₂ / n⁺- Si substrate
evaporated SiO₂
FDTS SAM
semiconductor layer F8T2
insulator layer PMMA

Fig. 5



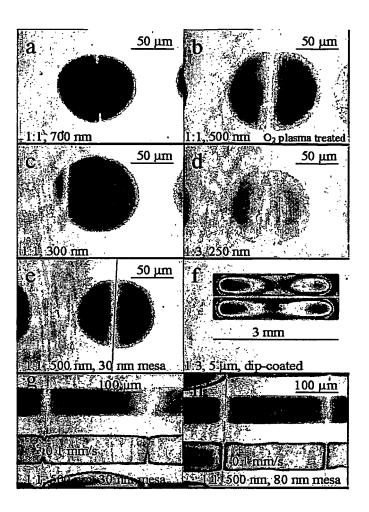


Fig. 6

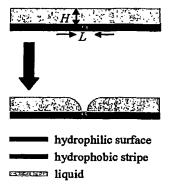


Fig. 7



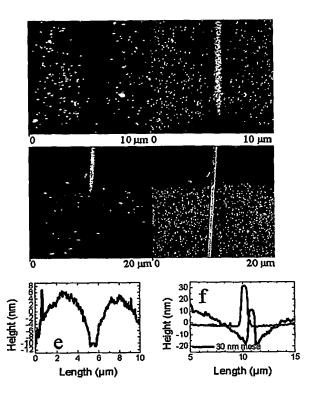


Fig. 8